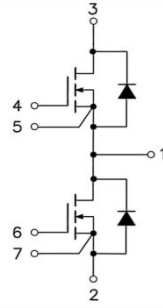


### 34mm Half Bridge SiC Module

Parameter	Value	Unit
$V_{DS}$	1200	V
$I_D$	130	A
$R_{DS(ON)}$	11.2	m $\Omega$
$Q_G$	500	nC



#### Features:

- High Current Density
- Low Inductive Design
- Low Switching Losses
- High-frequency Operation
- Zero Turn-off Tail Current from MOSFET

#### Applications:

- High Power Converters
- Motor Drives
- UPS Systems

#### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-source Voltage	1200	V
$V_{GS}$	Gate-source Voltage	-10/+22	V
$I_D$	Drain Current (continuous) ( $T_C=25^\circ\text{C}$ )	210	A
$I_D$	Drain Current (continuous) ( $T_C=90^\circ\text{C}$ )	130	A
$I_{DM}$	Drain Current (pulsed)	400	A
$T_{op}; T_{stg}$	Operating and Storage Temperature Range	-40 to +150	$^\circ\text{C}$
$T_J$	Junction Temperature	175	$^\circ\text{C}$
$R_{th(j-c)}$	Thermal Resistance, Junction-to- heat sink	0.14	$^\circ\text{C}/\text{W}$

#### MOSFET Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
Static characteristics (at $T_C=25^\circ\text{C}$ unless otherwise specified)						
$B_{VDS}$	Drain-source Breakdown Voltage	1200	-	-	V	$V_{GS}=0\text{V}$
$I_{DSS}$	Zero Gate Voltage Drain Current	-	-	40	$\mu\text{A}$	$V_{DS}=1200\text{V}; V_{GS}=0\text{V}$
$I_{GSS}$	Gate-body Leakage Current	-	-	2.0	$\mu\text{A}$	$V_{GS}=-10/+20\text{V}; V_{DS}=0\text{V}$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	-	4.0	V	$V_{DS}=V_{GS}; I_D=40\text{mA}$
$R_{DS(on)}$	Static Drain-source on Resistance	-	11.2	13	m $\Omega$	$V_{GS}=18\text{V}; I_D=80\text{A}; T_J=25^\circ\text{C}$
		-	20.2	-	m $\Omega$	$V_{GS}=18\text{V}; I_D=80\text{A}; T_J=175^\circ\text{C}$
$V_{GS(on)}$	Recommended Turn-on Voltage	-	18	-	V	Static
$V_{GS(off)}$	Recommended Turn-off Voltage	-	-5	-	V	
$R_G$	Gate Resistance	-	1.3	-	$\Omega$	$V_{GS}=0\text{V}; f=1\text{MHz}$

Dynamic characteristics (at TC=25°C unless otherwise specified)						
C <sub>iss</sub>	Input Capacitance	-	10.3	-	nF	V <sub>DS</sub> =1000V; f=1MHz; V <sub>AC</sub> =25mV
C <sub>oss</sub>	Output Capacitance	-	0.44	-		
C <sub>rss</sub>	Reverse Transfer Capacitance	-	16	-	pF	
E <sub>on</sub>	Turn-on Switching Energy	-	8.2	-	mJ	V <sub>DD</sub> =800V; V <sub>GS</sub> =-5/+18V; I <sub>D</sub> =120A; R <sub>G(ext)</sub> =5Ω Load=100μH
E <sub>off</sub>	Turn-off Switching Energy	-	5.8	-		
Q <sub>GS</sub>	Gate-Source Charge	-	128	-	nC	V <sub>DD</sub> =800V; V <sub>GS</sub> =-5/+18V; I <sub>D</sub> =80A
Q <sub>GD</sub>	Gate-drain Charge	-	132	-		
Q <sub>G</sub>	Total Gate Charge	-	500	-		
t <sub>d(on)</sub>	Turn-on Delay Time	-	77	-	ns	V <sub>DD</sub> =800V; V <sub>GS</sub> =-5/+18V; I <sub>D</sub> =120A; R <sub>G(ext)</sub> =5Ω Load=100μH
t <sub>r</sub>	Rise Time	-	55	-		
t <sub>d(off)</sub>	Turn-off Delay Time	-	193	-		
t <sub>f</sub>	Fall Time	-	43	-		

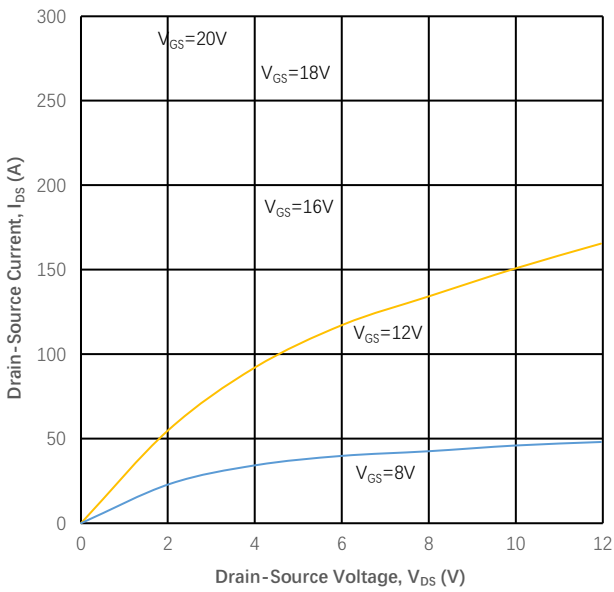
**Body Diode Characteristics (T<sub>J</sub> =25°C unless otherwise specified)**

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
V <sub>FSD</sub>	Forward Voltage	-	-	6	V	V <sub>GS</sub> =0V; I <sub>F</sub> =120A
I <sub>S</sub>	Continuous Diode Forward Current	-	120	-	A	V <sub>GS</sub> =0V; T <sub>C</sub> =25°C
T <sub>RR</sub>	Reverse Recovery Time	-	27	-	ns	V <sub>GS</sub> =-5/+18V; I <sub>F</sub> =120A; V <sub>R</sub> =800V
Q <sub>RR</sub>	Reverse Recovery Charge	-	2848	-	nC	
I <sub>RRM</sub>	Peak Reverse Recovery Current	-	39	-	A	

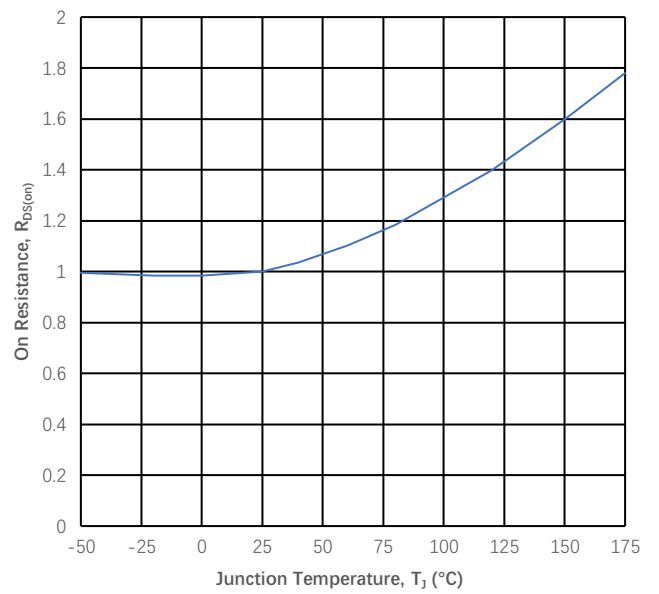
**Module Physical Characteristics**

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
V <sub>ISOL</sub>	Isolation Test Voltage	-	3.0	-	kV	f=50Hz; t=1min
L <sub>Stray</sub>	Stray Inductance	-	30	-	nH	
W	Weight	-	160	-	g	
M	Mounting Torque	2.5	-	5.0	N·m	M5

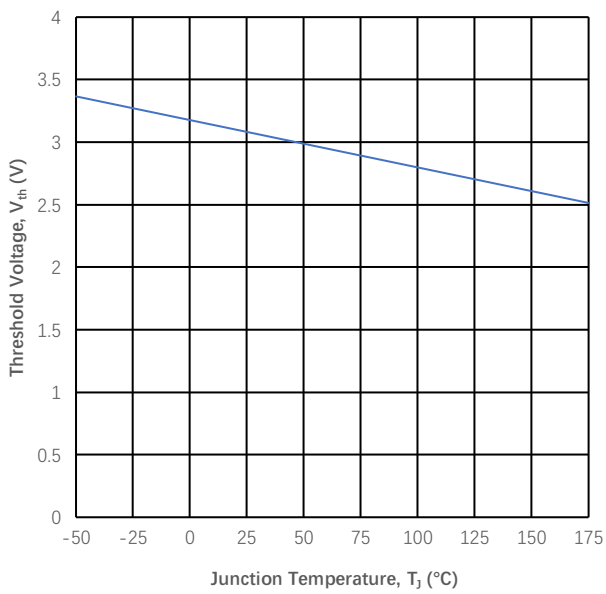
**Typical Characteristics**



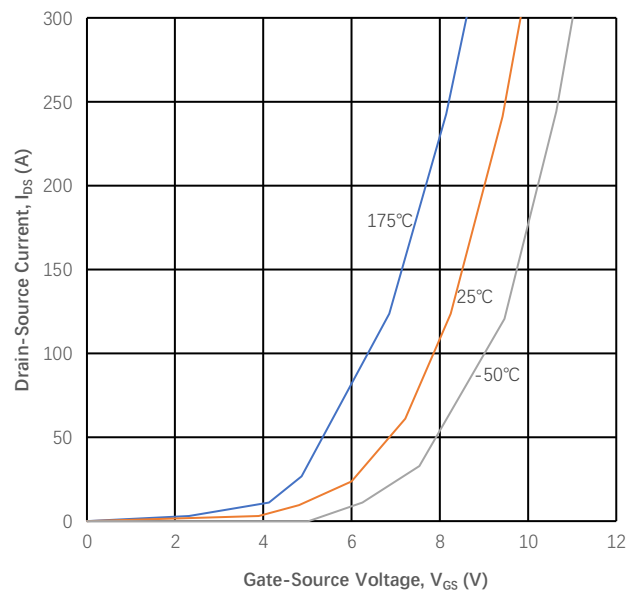
**Figure 1**  
 Output Characteristics ( $T_J=25\text{ }^\circ\text{C}$ )



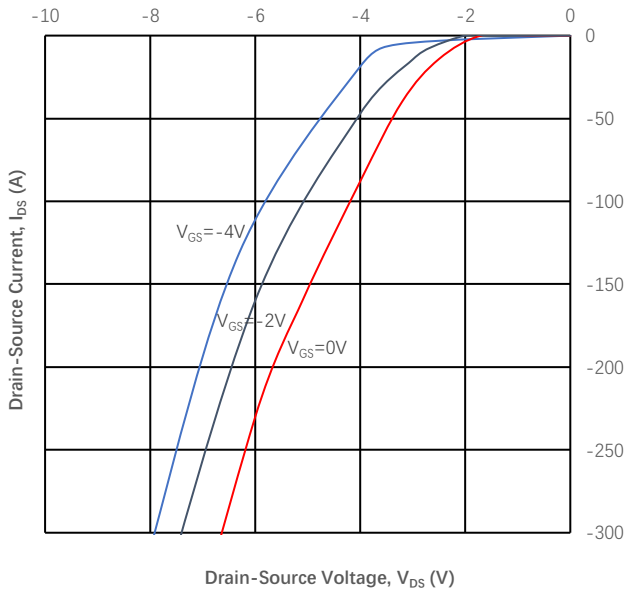
**Figure 2**  
 Normalized On-Resistance vs. Temperature



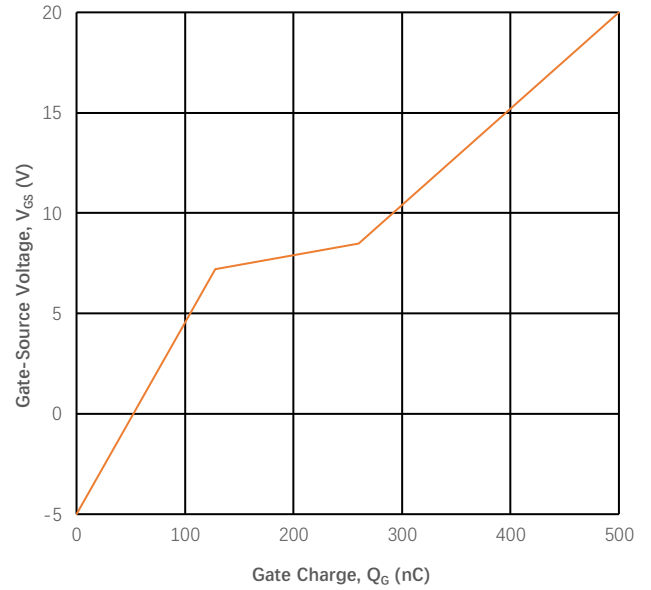
**Figure 3**  
 Threshold Voltage vs. Temperature



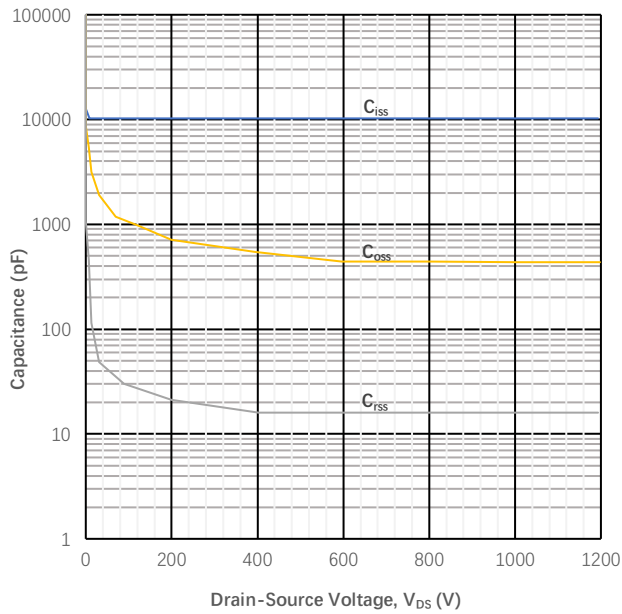
**Figure 4**  
 Transfer Characteristic for Various  $T_J$



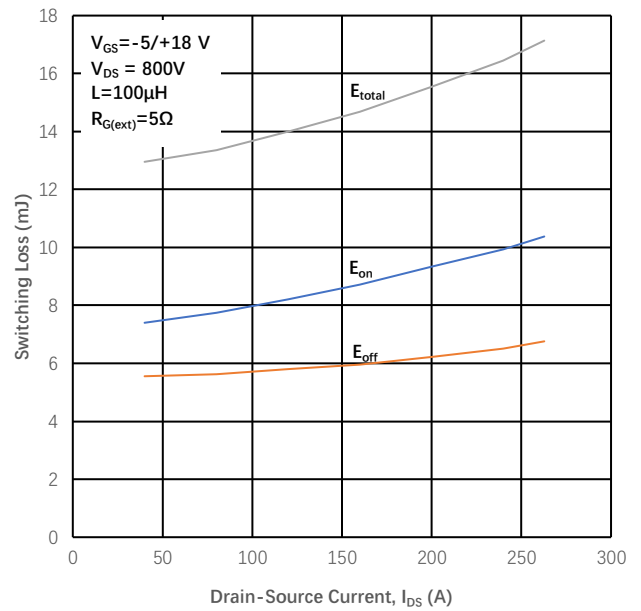
**Figure 5**  
 Body Diode Characteristic



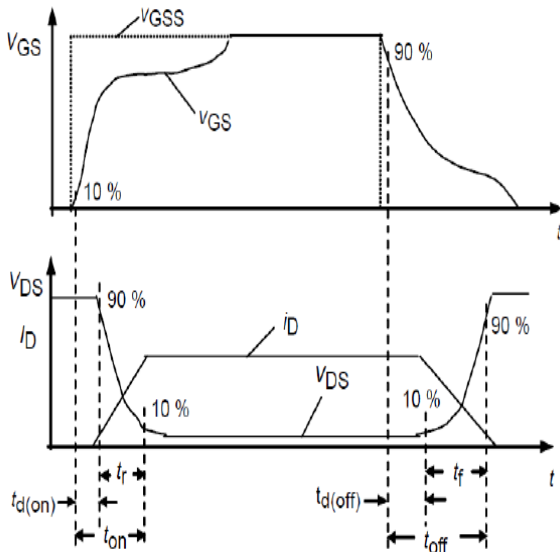
**Figure 6**  
 Typical Gate Charge Characteristics



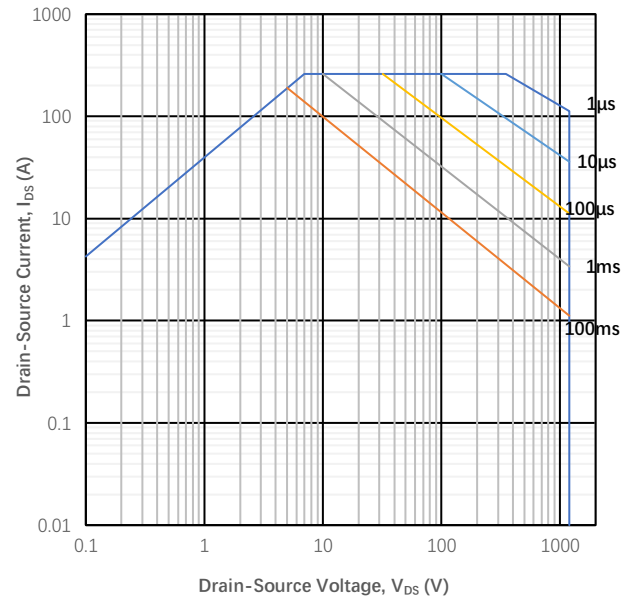
**Figure 7**  
 Typical Capacitances vs. Drain-Source Voltage



**Figure 8**  
 Inductive Switching Energy vs. Drain Current

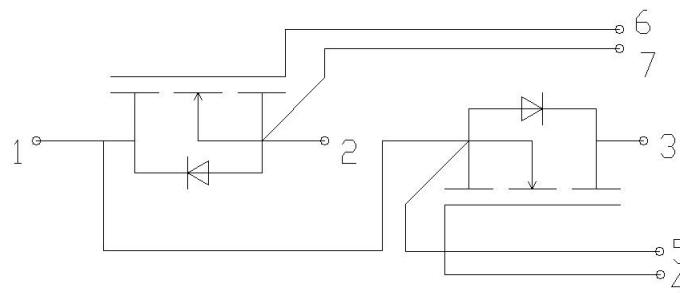


**Figure 9**  
 Switching Time Description

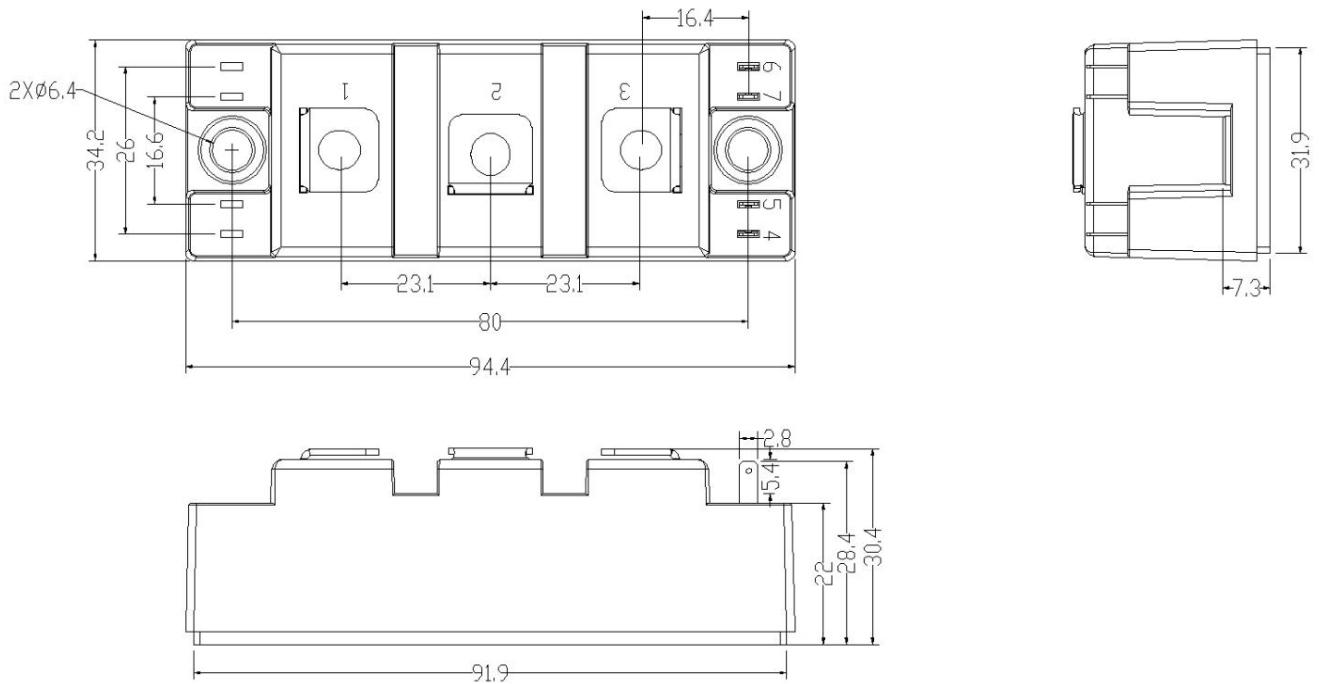


**Figure 10**  
 Safe Operating Area

**Circuit Diagram**



**Package Outlines(Unit: mm):**



**\*Important Usage Information and Disclaimer**

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