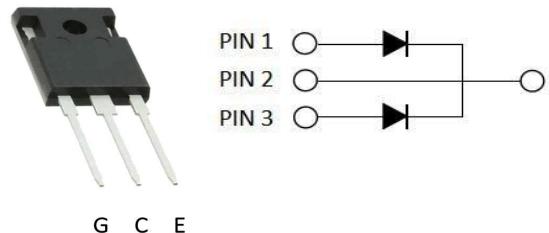


Silicon Carbide Schottky Diode

Product Summary

Parameter	Value	Unit
VRRM	1200	V
IF	53	A
VF	1.5	V



Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VRRM	Repetitive Peak Reverse Voltage	1200	V		
VRSM	Surge Peak Reverse Voltage	1200	V		
VDC	DC Blocking Voltage	1200	V		
IF	Continuous Forward Current	53*	A	TC=25°C	Fig. 7
		27*		TC=125°C	
		20*		TC=137°C	
IFRM	Repetitive Peak Forward Surge Current	100*	A	TC=25°C, tP=10 ms, Half Sine Wave,D=0.3	
IFSM	Non-Repetitive Peak Forward Surge Current	140*	A	TC=25°C, tP=10 ms, Half Sine Wave,D=0.3	
IF,Max	Non-Repetitive Peak Forward Surge Current	1200*	A	TC=25°C, tP= 10 μs, Pulse TC=110°C, tP= 10 μs, Pulse	
Ptot	Power Dissipation	283*	W	TC=25°C	Fig. 6
		123*		TC=110°C	
TJ , Tstg	Operating Junction and Storage Temperature	-55 to+175	°C		

Electrical Characteristics (Per Leg)

VF	Forward Voltage	1.5	1.8	V	IF = 20 A, TJ=25°C	Fig. 1
		2.2	3		IF = 20A, TJ=175°C	
IR	Reverse Current	10	50	μA	VR = 1200V, TJ=25°C	Fig. 2
		20	100		VR = 1200V, TJ=175°C	
QC	Total Capacitive Charge	95		nC	VR = 800 V,TJ = 25°C Qc=∫VR C V dV	Fig. 4
C	Total Capacitance	1430		pF	VR = 0 V, TJ = 25°C, f = 1 MHz	Fig. 3
		89			VR = 400 V, TJ = 25°C,f=1 MHz	
		65			VR = 800 V, TJ = 25°C,f=1 MHz	
EC	Capacitance Stored Energy	50		μJ	VR = 800 V	Fig. 5

Thermal Characteristics(Per Leg)

Symbol	Parameter	Typ.	Unit	Note
R _{arc}	Thermal Resistance from Junction to Case	0.53	C/W	Fig.8

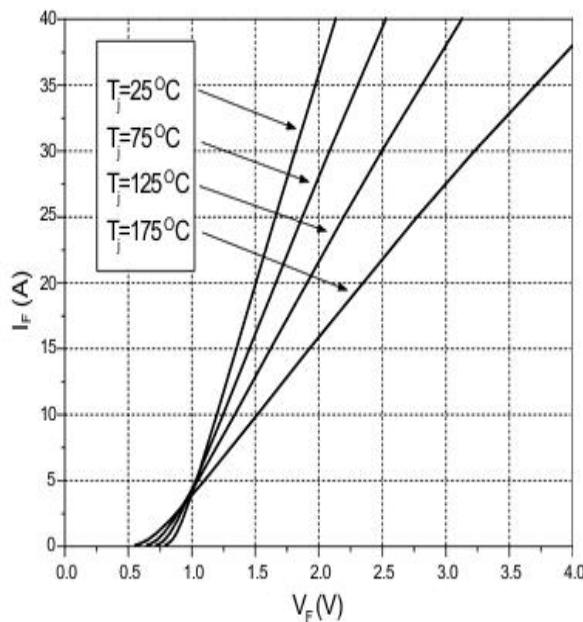
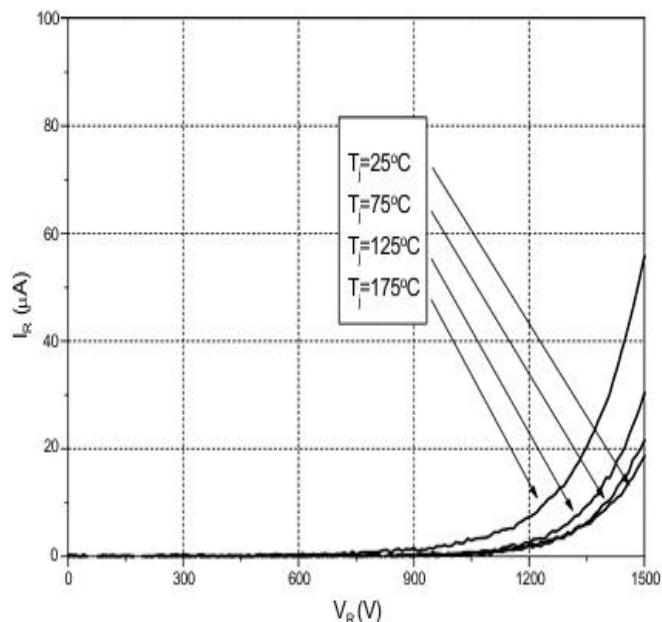
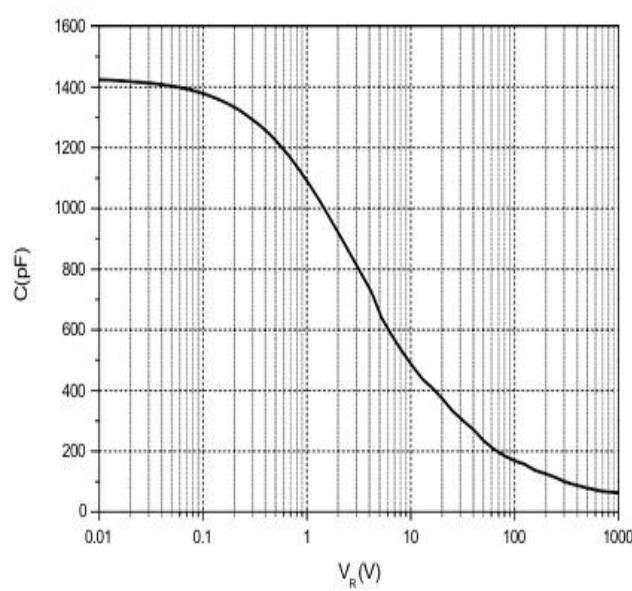
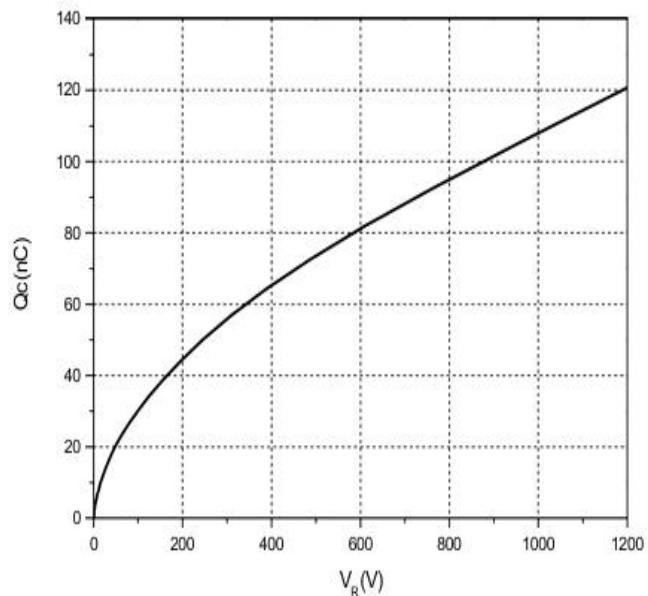
Characteristics diagrams
Figure 1.Forward Characteristics

Figure 2.Reverse Characteristics

Figure 3.Capacitance vs.Reverse Voltage

Figure 4.Total Capacitance Charge vs.Reverse Voltage


Figure 5.Capacitance Stored Energy

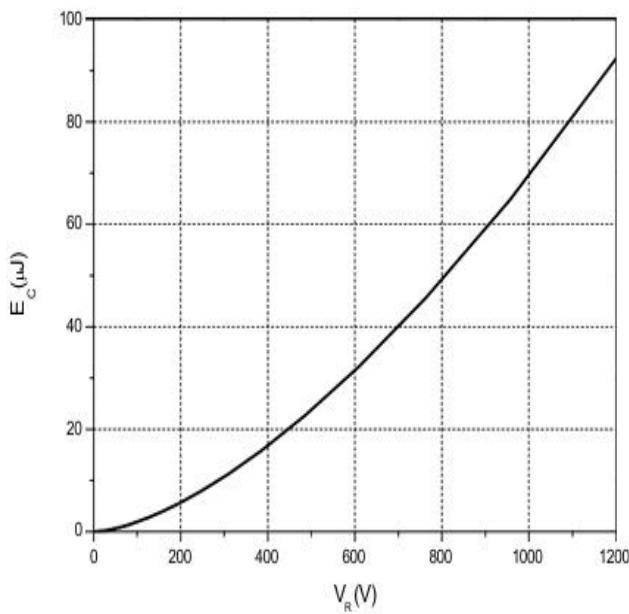


Figure 6. Power Derating

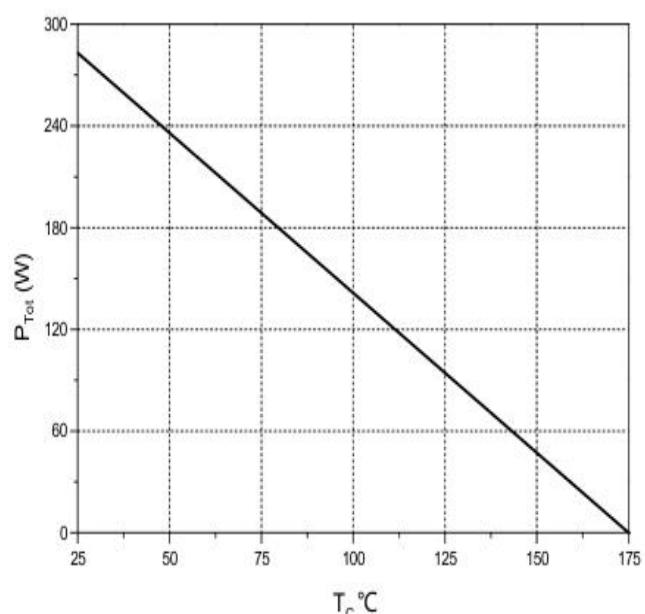


Figure 7.Current Derating

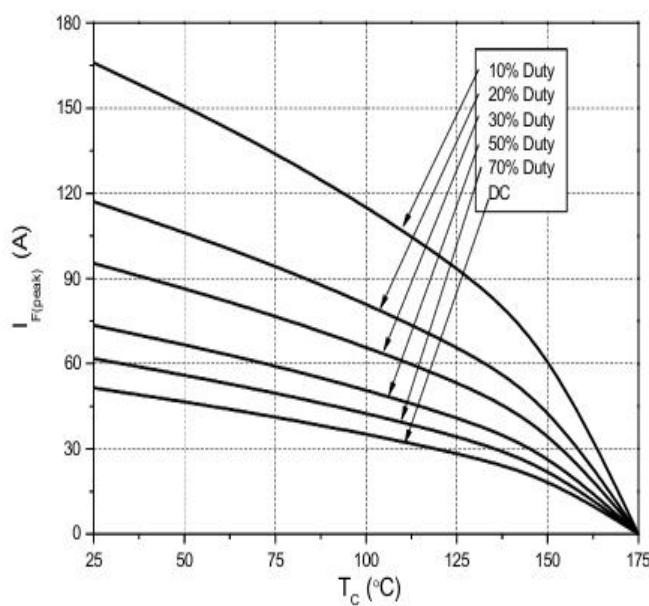
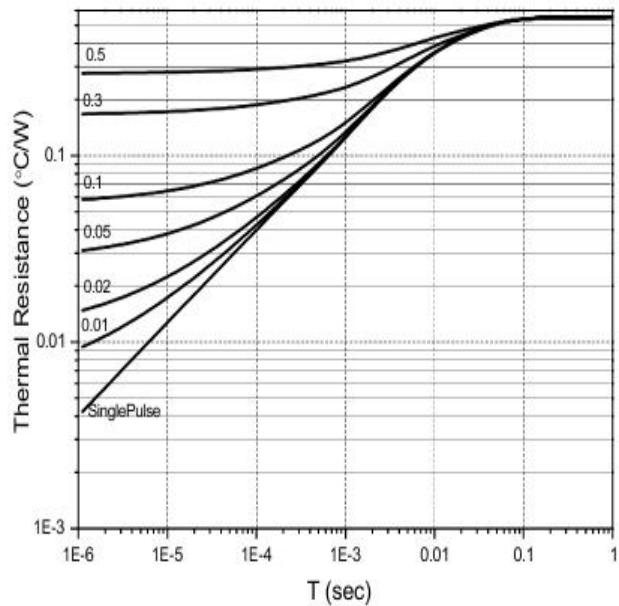
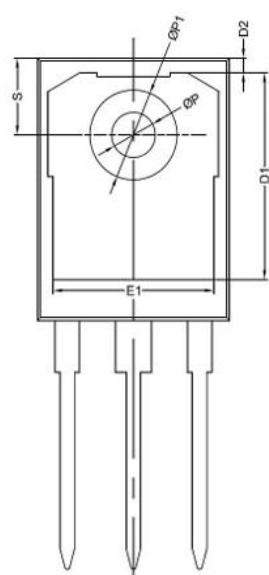
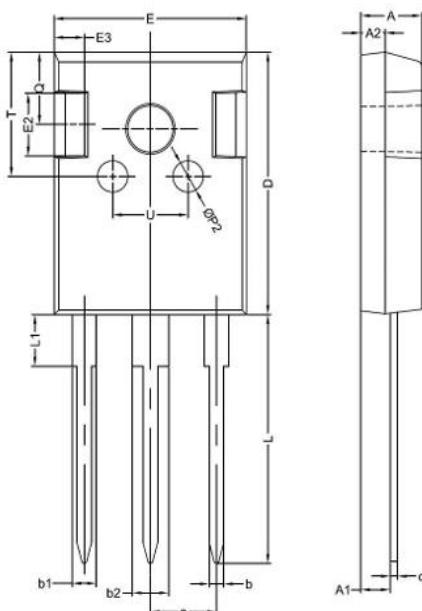


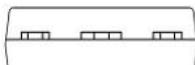
Figure 8.Transient Thermal Impedance



Package Dimensions:



符号	机械尺寸/mm		
	最小值	典型值	最大值
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.90	2.00	2.10
b	1.10	1.20	1.35
b1		2.00	
b2		3.00	
c	0.55	0.60	0.75
D	20.80	21.00	21.20
D1		16.55	
D2		1.20	
E	15.60	15.80	16.0
E1		13.30	
E2		5.00	
E3		2.50	
e		5.44	
L	19.42	19.92	20.42
L1		4.13	
P	3.50	3.60	3.70
P1	-	-	7.40
P2		2.50	
Q		5.80	
S	6.05	6.15	6.25
T		10.00	
U		6.20	



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- 1) Comply with applicable safety standards in designing a secure system architecture;
- 2) Implement redundancy, fire-prevention measures, and malfunction prevention protocols;
- 3) Mitigate risks of accidents, fires, or societal damages resulting from product use.
- 4) Designers must ensure Hypersemi products operate strictly within specified parameters defined in the latest product specifications.